



MALINENI LAKSHMAIAH WOMEN'S ENGINEERING COLLEGE (AUTONOMOUS)
(Accredited by "NBA" & NAAC with A+ Grade | Approved by AICTE, New Delhi & Affiliated to
JNTUK, Kakinada)
Pulladigunta (Vil), Vatticherukuru (Md), Prathipadu Road, Guntur - 522 017 A.P.

Ref. No.: MLWEC/Autonomous/CoE Office/2023-24/Timetables/26

12.02.2024

M.Tech. (VLSI) I Year I Semester (MR23 REGULATION)
II-MIDTERM EXAMINATIONS, FEBRUARY 2024
TIME TABLE

S. No.	Date (Day)	Course Code	Course Name	Timings
1	19.02.2024 (MONDAY)	23MTVLT01	CMOS Analog IC Design (AICD)	10:00 am - 12:00 Noon
2	20.02.2024 (TUESDAY)	23MTVLT02	CMOS Digital IC design(DICD)	10:00 am - 12:00 Noon
3	21.02.2024 (WEDNESDAY)	23MTVLE01	VLSI Technology (VLSIT)	10:00 am - 12:00 Noon
4	22.02.2024 (THURSDAY)	23MTVLE06	Photonics (PH)	10:00 am - 12:00 Noon
5	23.02.2024 (FRIDAY)	23MTVLT03	Research methodology and IPR (RMIP)	10:00 am - 12:00 Noon

David

Controller of Examinations
CONTROLLER OF EXAMINATIONS
MALINENI LAKSHMAIAH
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Copy to: ECE-HOD, Coordinator - M.Tech.(VLSI), circulation to all the I Year I Sem M.Tech.(VLSI) students and the Faculty members concerned

Received copy
[Signature]